ALU Test V2

Version 2.0 of the ALU has implemented all the R and I type RV32I instructions.

The last test tested ADD SUB OR AND, the new test will test the rest of the instructions.

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| --- | --- |
| Instruction | Function |
| SLL | Shift Left |
| SLT | Set less than |
| SLTU | Set less than unsigned |
| XOR | XOR |
| SRL | Shift right logarithmic |
| SRA | Shift right arithmetic |
|  |  |

PmThe tests should be

Default case test – set aluop to 3

Shift left – A = 00A0, B = 0005

Set less than A and B the same then switch

Same for SLTU

XOR A and B

SRL A and B

SRA 80A0 0005

Currently all operations are functional except shift right arithmetic

Divid